| INFORMATION DISCLOSURE CITATION (Use several sheets if necessary) | ATTY DOCKET NO. BUR920000202US1 | SERIAL NO. |
|---|---|---|
| | APPLICANT(S) Jerry D. Hayes | |
| | FILING DATE | GROUP |

OIPE FEB 11 2002 PATENT & TRADEMARK OFFICE

**U.S. PATENT DOCUMENTS**

| EXAMINER INITIAL | | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE IF APPROPRIATE |
|---|---|---|---|---|---|---|---|
| | AA | 5757697 | 05/26/98 | Sawai et al. | 364 | 578 | Jul. 23, 1996 |
| | AB | 5675502 | 10/07/97 | Cox | 364 | 490 | Aug. 22, 1995 |
| | AC | 5652716 | 07/29/97 | Battersby | 364 | 578 | Aug. 10, 1994 |
| | AD | 5535146 | 07/09/96 | Huang et al. | 364 | 578 | Apr. 27, 1995 |
| | AE | 6110219 | 08/29/00 | Jiang | 716 | 1 | Jul. 24, 1997 |
| | AF | 6080201 | 06/27/00 | Hojat et al. | 703 | 14 | Feb. 10, 1998 |
| | AG | 6035115 | 03/07/00 | Suzuki | 395 | 500.36 | Feb. 25, 1998 |
| | AH | 6005829 | 12/21/99 | Conn | 368 | 118 | May 21, 1998 |
| | AI | 5949983 | 09/07/99 | Baxter | 395 | 500 | Apr. 18, 1996 |
| | AJ | 5920489 | 07/06/99 | Dibrino et al. | 364 | 578 | May 3, 1996 |
| | AK | 5838947 | 11/17/98 | Sarin | 395 | 500 | Apr. 2, 1996 |

**FOREIGN PATENT DOCUMENTS**

| | | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUBCLASS | TRANSLATION YES | TRANSLATION NO |
|---|---|---|---|---|---|---|---|---|
| | | | | | | | | |
| | | | | | | | | |
| | | | | | | | | |
| | | | | | | | | |
| | | | | | | | | |

**OTHER DOCUMENTS** *(Including Author, Title, Date, Pertinent Pages, Etc.)*

| | | | |
|---|---|---|---|
| | | | |
| | | | |

| EXAMINER | DATE CONSIDERED 24-FEB-2003 |
|---|---|

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

| INFORMATION DISCLOSURE CITATION *(Use several sheets if necessary)* | ATTY DOCKET NO. BUR920000[illegible]US1 | SERIAL NO. |
|---|---|---|
| | APPLICANT(S) Jerry D. Hayes | |
| | FILING DATE | GROUP |

OIPE JC64 FEB 11 2002 PATENT & TRADEMARK OFFICE

**U.S. PATENT DOCUMENTS**

| *EXAMINER INITIAL | | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE IF APPROPRIATE |
|---|---|---|---|---|---|---|---|
| | AL | 5706477 | 01/06/98 | Goto | 395 | 500 | Nov. 14, 1995 |
| | AM | 5696694 | 12/09/97 | Khouja et al. | 364 | 488 | Jun. 2, 1995 |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |

**FOREIGN PATENT DOCUMENTS**

| | | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUBCLASS | TRANSLATION YES | TRANSLATION NO |
|---|---|---|---|---|---|---|---|---|
| | | | | | | | | |
| | | | | | | | | |
| | | | | | | | | |
| | | | | | | | | |
| | | | | | | | | |

**OTHER DOCUMENTS** *(Including Author, Title, Date, Pertinent Pages, Etc.)*

| | | | |
|---|---|---|---|
| | | | |
| | | | |

| EXAMINER | DATE CONSIDERED 24-FEB-2003. |
|---|---|

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

# INFORMATION DISCLOSURE CITATION

*(Use several sheets if necessary)*

MAY 0 6 2002
PATENT & TRADEMARK OFFICE

| Docket Number (Optional) | Application Number |
|---|---|
| BUR920000[redacted]2 | 09/683,546 |
| **Applicant(s)** Jerry D. Hayes | |
| **Filing Date** 01/16/02 | **Group Art Unit** |

## U.S. PATENT DOCUMENTS

| *EXAMINER INITIAL | REF | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE IF APPROPRIATE |
|---|---|---|---|---|---|---|---|
| | | 5,481,484 | 01/02/96 | Ogawa et al. | | | |
| | | 5,535,146 | 07/09/96 | Huang et al. | | | |
| | | 5,652,716 | 07/29/97 | Battersby et al. | | | |
| | | 5,675,502 | 10/07/97 | Cox | | | |
| | | 5,696,694 | 12/09/97 | Khouja et al. | | | |
| | | 5,706,477 | 01/06/98 | Goto | | | |
| | | 5,757,679 | 05/26/98 | Sawai et al. | | | |
| | | 5,838,947 | 11/17/98 | Sarin | | | |
| | | 5,920,489 | 07/06/99 | Dibrino et al. | | | |
| | | 5,949,983 | 09/07/99 | Baxter | | | |
| | | 6,005,829 | 12/21/99 | Conn | | | |

## FOREIGN PATENT DOCUMENTS

| | REF | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUBCLASS | Translation YES | Translation NO |
|---|---|---|---|---|---|---|---|---|
| | | | | | | | | |
| | | | | | | | | |
| | | | | | | | | |
| | | | | | | | | |
| | | | | | | | | |

## OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

| | |
|---|---|
| | Bina Ackalloor, Dinesh Gaitonde, "An Overview of Library Characterization in Semi-Custom Design" 5/97 IEEE 1998 Custom Integrated Circuits Conference, pp. 305-312 |

| EXAMINER | DATE CONSIDERED |
|---|---|
| | 24-FEB-2003 |

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

# INFORMATION DISCLOSURE CITATION

*(Use several sheets if necessary)*

MAY 0 6 2002

PATENT & TRADEMARK OFFICE

| ATTY DOCKET NO. | SERIAL NO. |
|---|---|
| BUR9200002[illegible]2 | 09/683,546 |
| APPLICANT(S) | |
| Jerry D. Hayes | |
| FILING DATE | GROUP |
| 01/16/02 | |

## U.S. PATENT DOCUMENTS

| *EXAMINER INITIAL | | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE IF APPROPRIATE |
|---|---|---|---|---|---|---|---|
| | | 6,035,115 | 03/07/00 | Suzuki | | | |
| | | 6,080,201 | 06/27/00 | Hojat, et al. | | | |
| | | 6,102,960 | 08/15/00 | Berman, et al. | | | |
| | | 6,110,219 | 08/29/00 | Jiang | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |

## FOREIGN PATENT DOCUMENTS

| | | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUBCLASS | TRANSLATION YES | TRANSLATION NO |
|---|---|---|---|---|---|---|---|---|
| | | | | | | | | |
| | | | | | | | | |
| | | | | | | | | |
| | | | | | | | | |
| | | | | | | | | |

## OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

| | | | |
|---|---|---|---|
| | | | Chandrakasan et al., "Design of High-Performance Microprocessor Circuits" ISBN 0-7803-6001-X, IEEE Order No. PC5836, Chapter 16.3 pp. 338-345 |
| | | | Jessica Qian, et al., "Modeling the 'Effective Capacitance' for the RC Interconnect of CMOS Gates" December 1994, Volume 13. No. 12, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems pp. 1526-1535 |

EXAMINER

DATE CONSIDERED 24-FEB-2003

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.